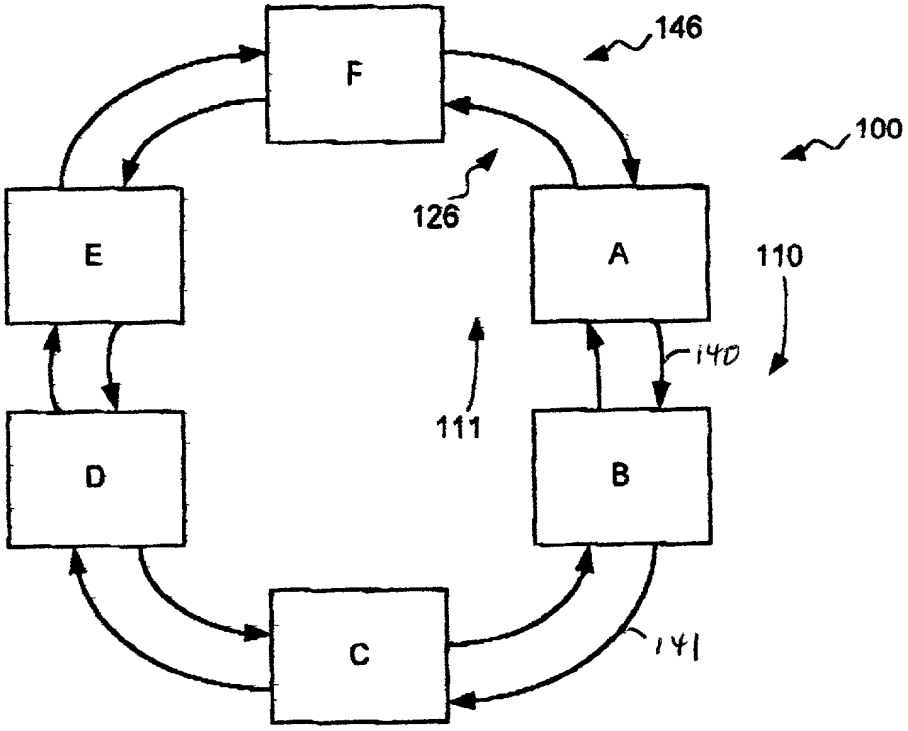

Fig. 1

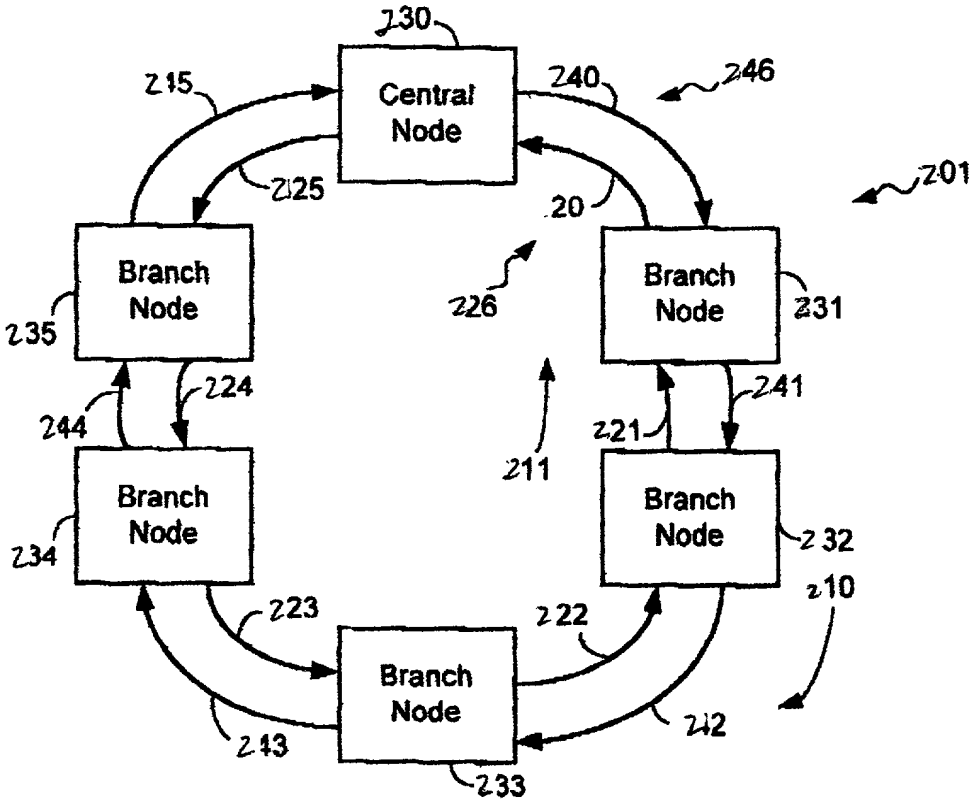
230
Central Node
215
240
246
201
225
20
Branch Node
235
226
Branch Node
231
224
241
244
221
211
Branch Node
234
Branch Node
232
223
222
210
Branch Node
243
242
233

Fig. 2

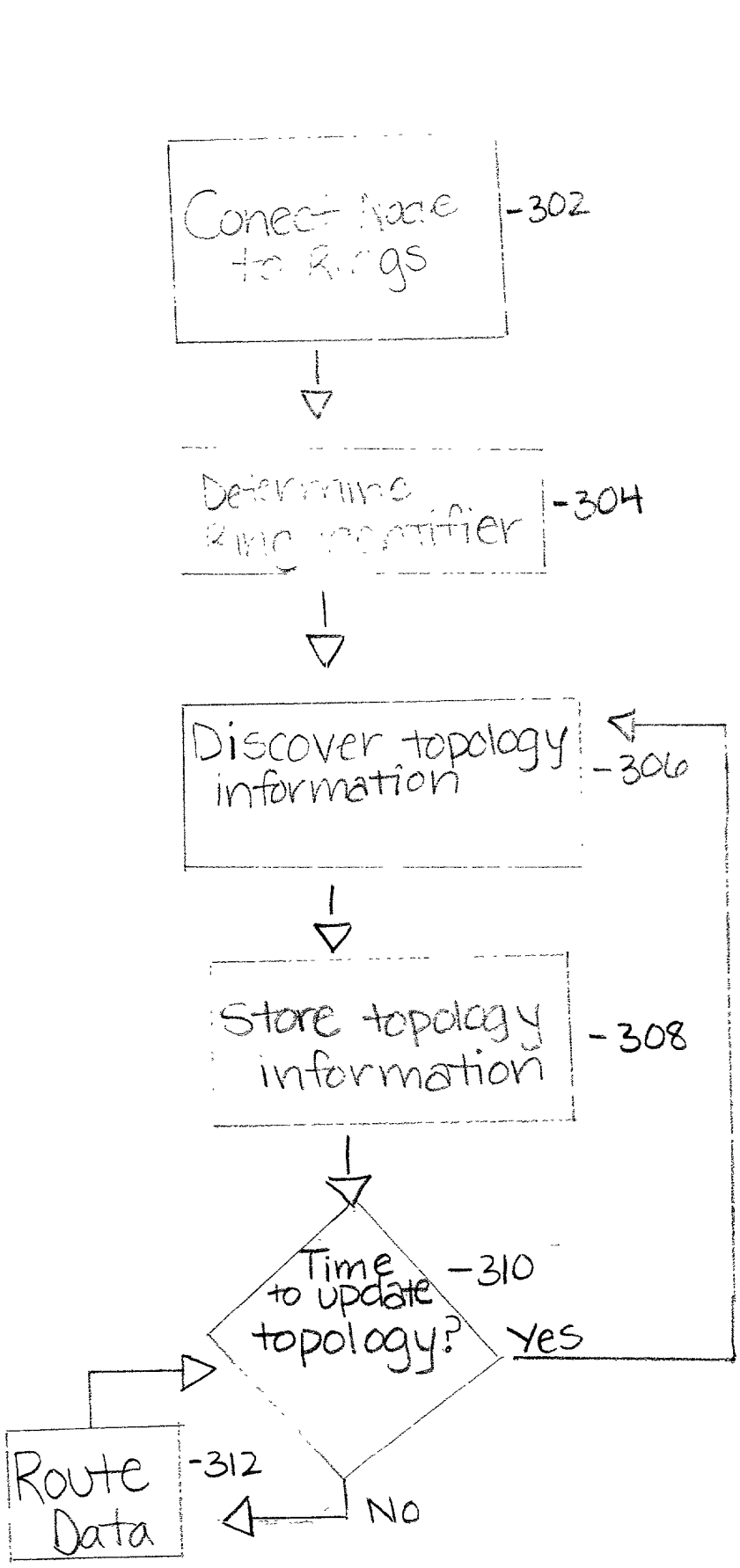
300
Connect face to Rings
302
Determining Ring identifier
304
Discover topology information
306
Store topology information
308
Time to update topology?
310
Yes
No
Route Data
312

Fig 3a.

Exhibit 1009 - Page 63

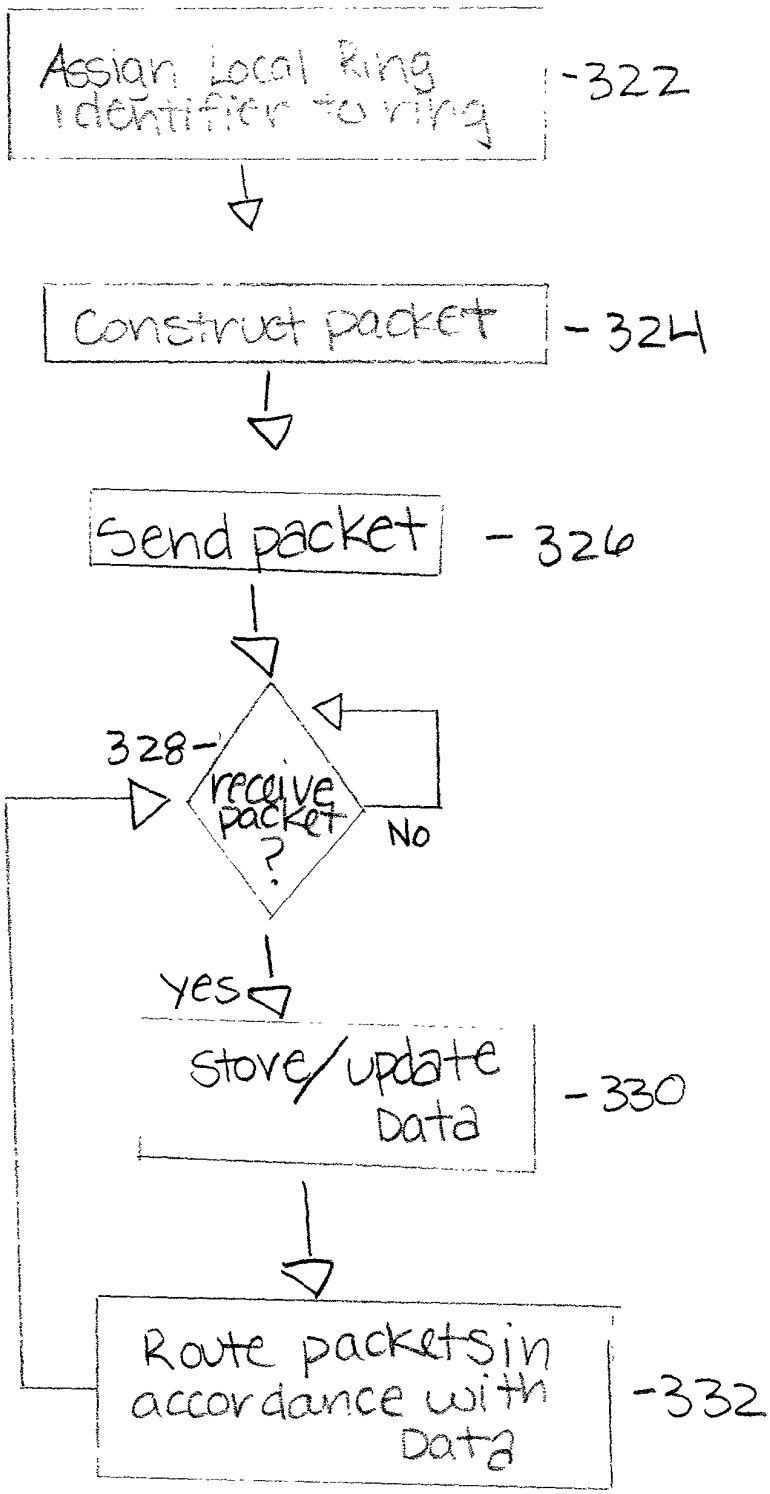

Fig 3b.

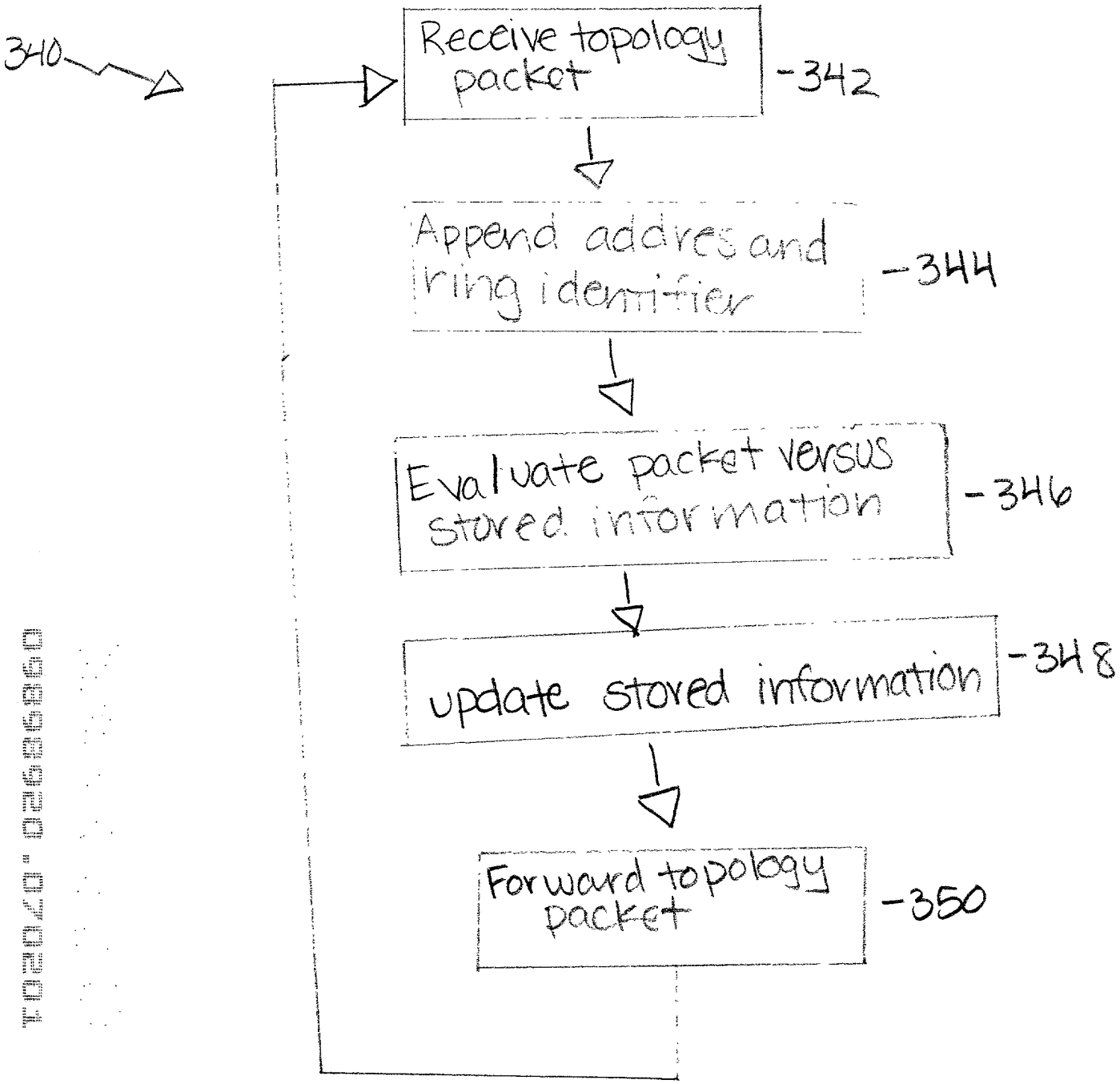
340
Receive topology packet
342
Append address and ring identifier
344
Evaluate packet versus stored information
346
update stored information
348
Forward topology packet
350

Fig 3c

TO2020"02686860

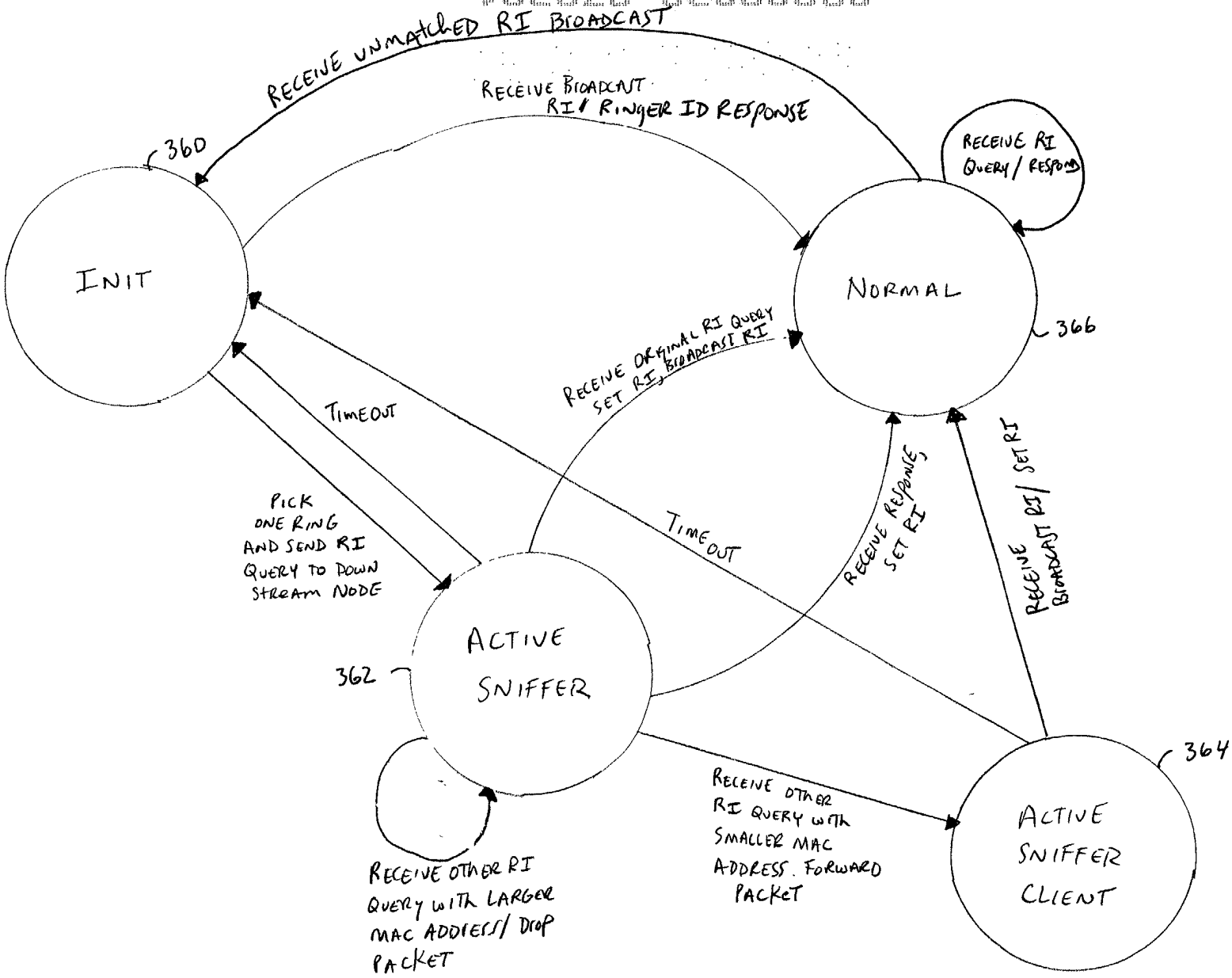

FIG. 3d

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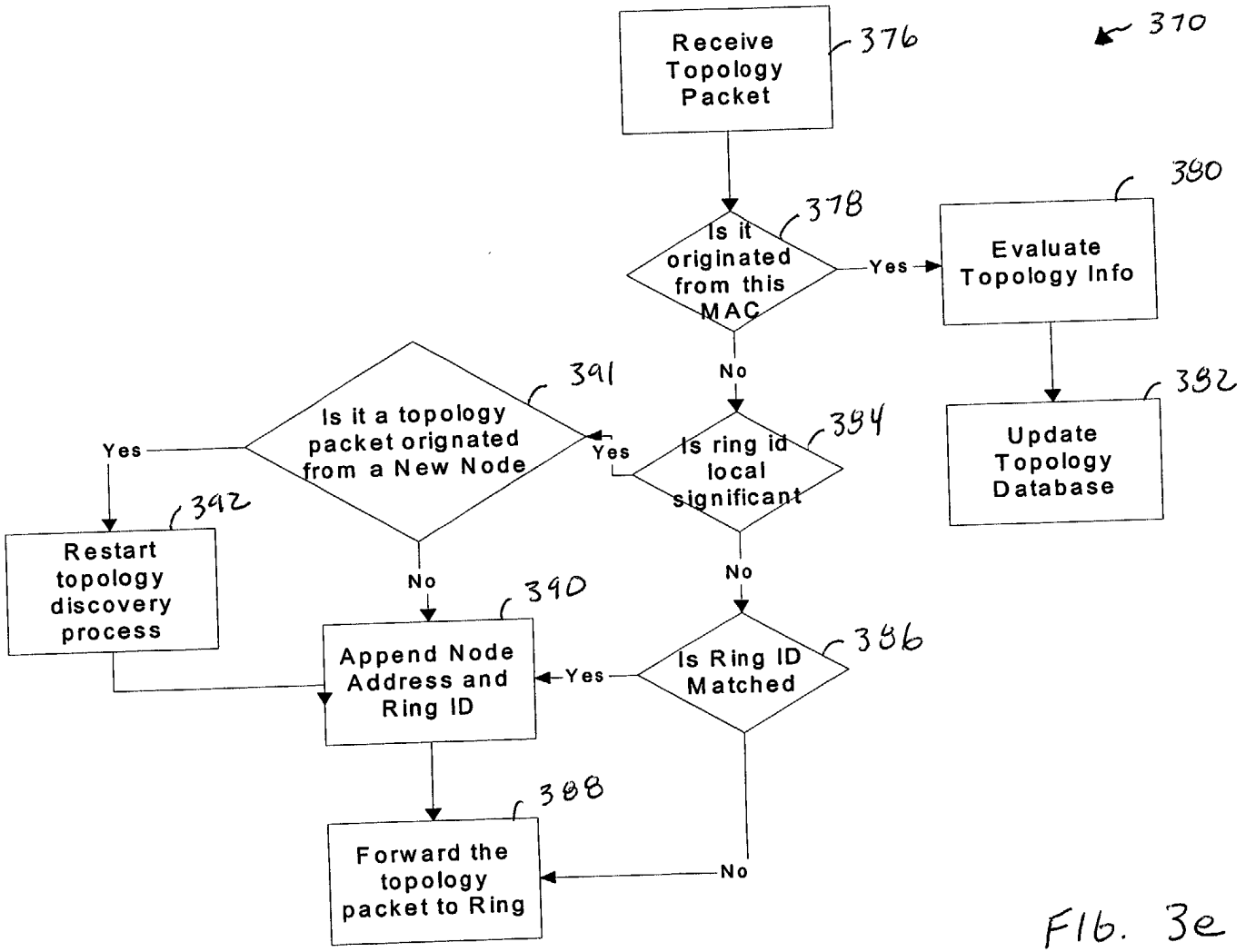

Fig. 3e

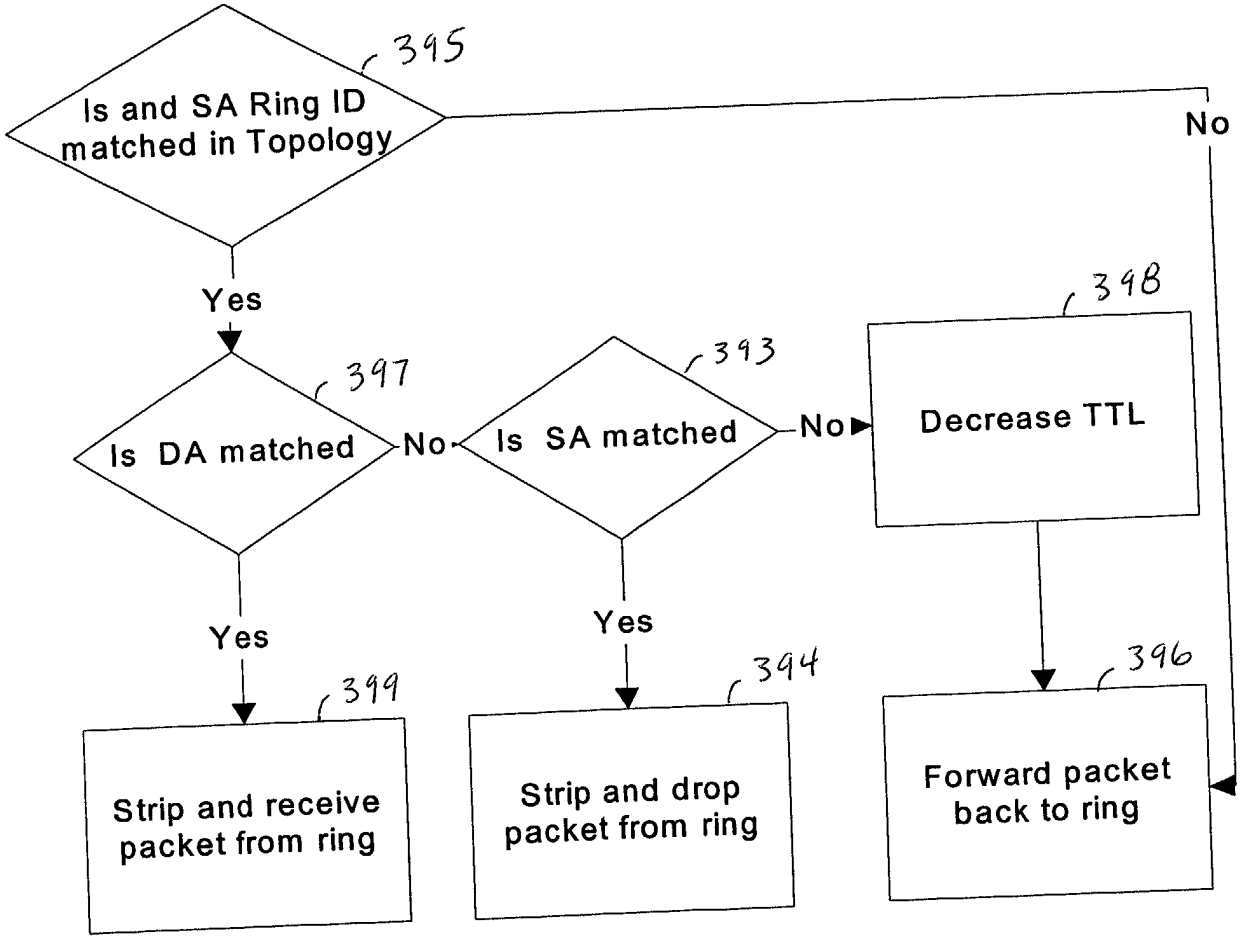
395
Is and SA Ring ID matched in Topology
No
Yes
397
Is DA matched
No
393
Is SA matched
No
398
Decrease TTL
Yes
Yes
399
Strip and receive packet from ring
394
Strip and drop packet from ring
396
Forward packet back to ring

Fig. 3f

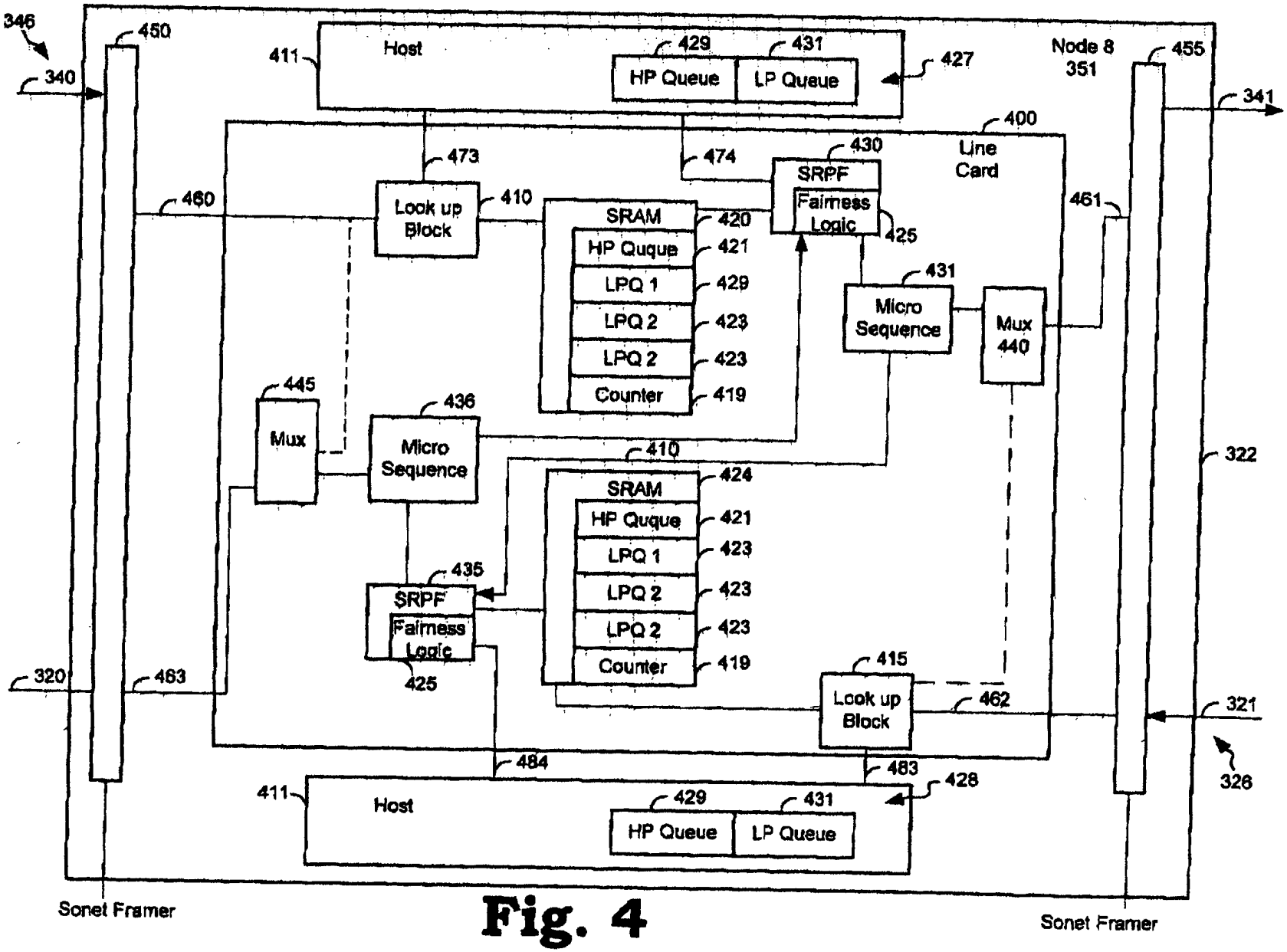
346
340
450
411
Host
429
431
HP Queue
LP Queue
427
Node 8
351
455
341
400
Line
Card
473
474
430
SRPF
Fairness
Logic
425
460
Look up
Block
410
SRAM
420
HP Quque
421
LPQ 1
429
LPQ 2
423
LPQ 2
423
Counter
419
461
431
Micro
Sequence
Mux
440
445
Mux
436
Micro
Sequence
410
322
SRAM
424
HP Quque
421
LPQ 1
423
LPQ 2
423
LPQ 2
423
Counter
419
435
SRPF
Fairness
Logic
425
320
463
415
Look up
Block
462
321
326
484
483
428
411
Host
429
431
HP Queue
LP Queue
Sonet Framer
Sonet Framer

Fig. 4

T020Z0"02686860

502 ADDRESS (MAC, IP)	504 HOP COUNT INNER	506 HOP COUNT OUTER	508 LATENCY INNER	510 LATENCY OUTER	512 STATIC RING DIRECTION	514 DYNAMIC RING DIRECTION	503 RING ID INNER	503 RING ID OUTER
12.23.46	10	8	1234	1456	I	O	I	O
12.23.47	9	9	1200	1400	I	I	I	O

FIG. 5